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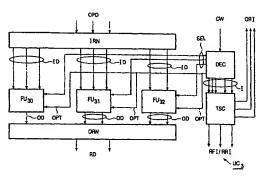
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[Continued on next page]

(54) Title: APPARATUS, METHOD, AND COMPILER ENABLING PROCESSING OF VARIABLE LENGTH INSTRUCTIONS IN A VERY LONG INSTRUCTION WORD PROCESSOR



(57) Abstract: Computer architectures consist of a fixed data path, which is controlled by a set of control words. Each control word controls part of the data path. Each set of instructions generates a new set of control words. In case of a VLIW processor, multiple instructions are packaged into one so-called VLIW instruction. A VLIW processor uses multiple, independent functional units to execute these multiple instructions in parallel. Application specific domain tuning of a VLIW processor requires that instructions having varying requirements with respect to the number of instruction bits they require can be encoded in a single VLIW instruction, such that an efficient encoding and encoding of instructions is maintained. The present invention describes a processing apparatus as well as a processing method for processing data, allowing the use of such an asymmetric instruction set. The processing apparatus comprises at least a first (UC₀) and a second issue slot (UC₃), wherein each issue slot comprises a plurality of functional units (FU₀₁ - FU₀₂, FU₃₀ - FU₃₂). The first issue slot (UC₀) is being controlled by a first control word (411) being generated from the second instruction (InstrA) and the second issue slot is being controlled by a second control word (417) being generated from the second instruction (InstrD), the width of the first control word (411) being different from the width of the second control word (417). By varying the width of a corresponding control word, instructions requiring a different number of bits can be efficiently encoded in a VLIW instruction while allowing an efficient instruction decoding as well.





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A. CLASSIFICATION OF SUBJECT MATTER IPC 7 G06F9/38 G06F G06F9/45 G06F9/30 According to International Patent Classification (IPC) or to both national classification and IPC **B. FIELDS SEARCHED** Minimum documentation searched (classification system followed by classification symbols) IPC 7 G06F Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal C. DOCUMENTS CONSIDERED TO BE RELEVANT Category ° Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. X EP 0 886 210 A (MATSUSHITA ELECTRIC IND CO 1,2,5,6, LTD) 23 December 1998 (1998-12-23) 8,9, 12-14 Y page 4, line 27 - line 44 3,4,10, Y WO 98/27486 A (HEWLETT PACKARD CO) 3,4,10, 25 June 1998 (1998-06-25) page 6, line 16 -page 8. line 21 X US 6 023 756 A (OKAMURA ATSUSHI) 1,2,5,6, 8 February 2000 (2000-02-08) column 1, line 31 - line 51; figure 1 Further documents are listed in the continuation of box C. Patent family members are listed in annex. Special categories of cited documents: *T* later document published after the international filing date "A" document defining the general state of the art which is not considered to be of particular relevance or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention earlier document but published on or after the international "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone filing date 'L' document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such docucitation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means ments, such combination being obvious to a person skilled document published prior to the international filing date but later than the priority date claimed *&* document member of the same patent family Date of the actual completion of the international search Date of mailing of the international search report 12 May 2004 01/06/2004 Name and mailing address of the ISA Authorized officer European Patent Office, P.B. 5818 Patentlaan 2 NL – 2280 HV Rijswijk Tel. (+31–70) 340–2040, Tx. 31 651 epo ni, Fax: (+31–70) 340–3016 Moraiti, M

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